

ISI

ISI Interface

**Technical
Information**

TR-Electronic GmbH

D-78647 Trossingen

Eglishalde 6

Tel.: (0049) 07425/228-0

Fax: (0049) 07425/228-33

email: info@tr-electronic.de

www.tr-electronic.com

Copyright protection

This Manual, including the illustrations contained therein, is subject to copyright protection. Use of this Manual by third parties in contravention of copyright regulations is not permitted. Reproduction, translation as well as electronic and photographic archiving and modification require the written content of the manufacturer. Violations shall be subject to claims for damages.

Subject to modifications

The right to make any changes in the interest of technical progress is reserved.

Document information

Release date / Rev. date:	04/01/2016
Document / Rev. no.:	TR - E - TI - GB - 0018 - 02
File name:	TR-E-TI-GB-0018-02.docx
Author:	MÜJ

Font styles

Italic or **bold** font styles are used for the title of a document or are used for highlighting.

`Courier` font displays text, which is visible on the display or screen and software menu selections.

" < > " indicates keys on your computer keyboard (such as <RETURN>).

Contents

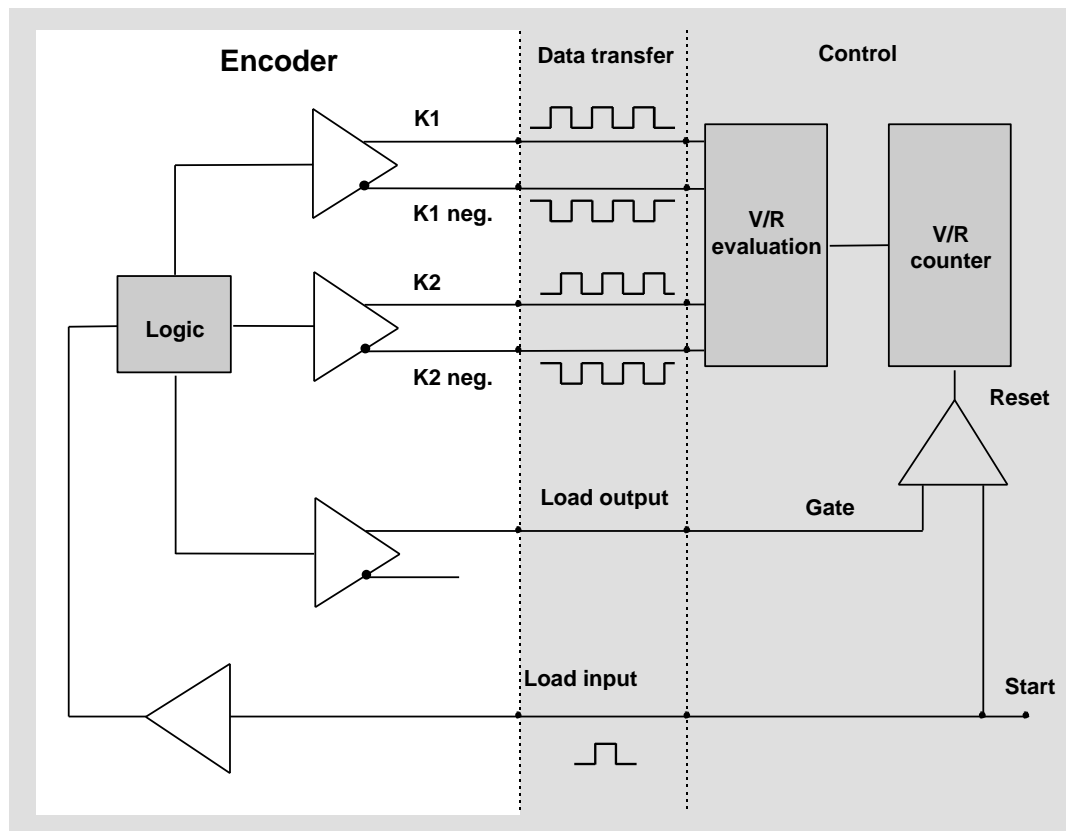
Contents	3
Revision index	4
1 Absolute incremental - serial - interface (ISI-Interface).....	5
1.1 Description	5
1.2 Definition of the number of steps of the incremental-interface and the pulse number resulting from it	6
1.3 Impulse diagram: loading process for falling loading edge	7

Revision index

Revision	Date	Index
First release	09/22/97	00
Definition of the number of steps of the incremental-interface and the pulse number resulting from it.	10/30/02	01
General modifications	04/01/16	02

1 Absolute incremental - serial - interface (ISI-Interface)

Block diagram



1.1 Description

Incremental serial interface for absolute encoders. Changes in position are transferred by two common incremental tracks. The sign of the 90° phase displacement indicates the direction of travel. However, it is not necessary to approach reference points.

Instead, the encoder's loading input is connected to the loading level. The encoder then no longer issues pulses, but switches on its loading output. The incremental counter can now be set to zero and the loading level disconnected from the loading input. The encoder then issues counter pulses until the incremental counter is counted up to the encoder position. When this position is reached, it disconnects its loading output again and is then ready for further loading operations.

Since during the loading operation the incremental counter does not contain a valid position, the axis should not be positioned during that period. To suppress glitches stored in the incremental counter, you should repeat the loading operation at regular intervals, for example when the respective axis is idle.

If the connected incremental counter frequently contains faulty values, you should reduce the maximum frequency which is programmed in the encoder. Apart from this, the system must have good interference suppression characteristics. The incremental signals should be transferred differentially with TTL levels via twisted-pair wires. All the cables, particularly the power cables on frequency converters, must be fitted with screens grounded on both ends (consider installation hints in the associated manual !).

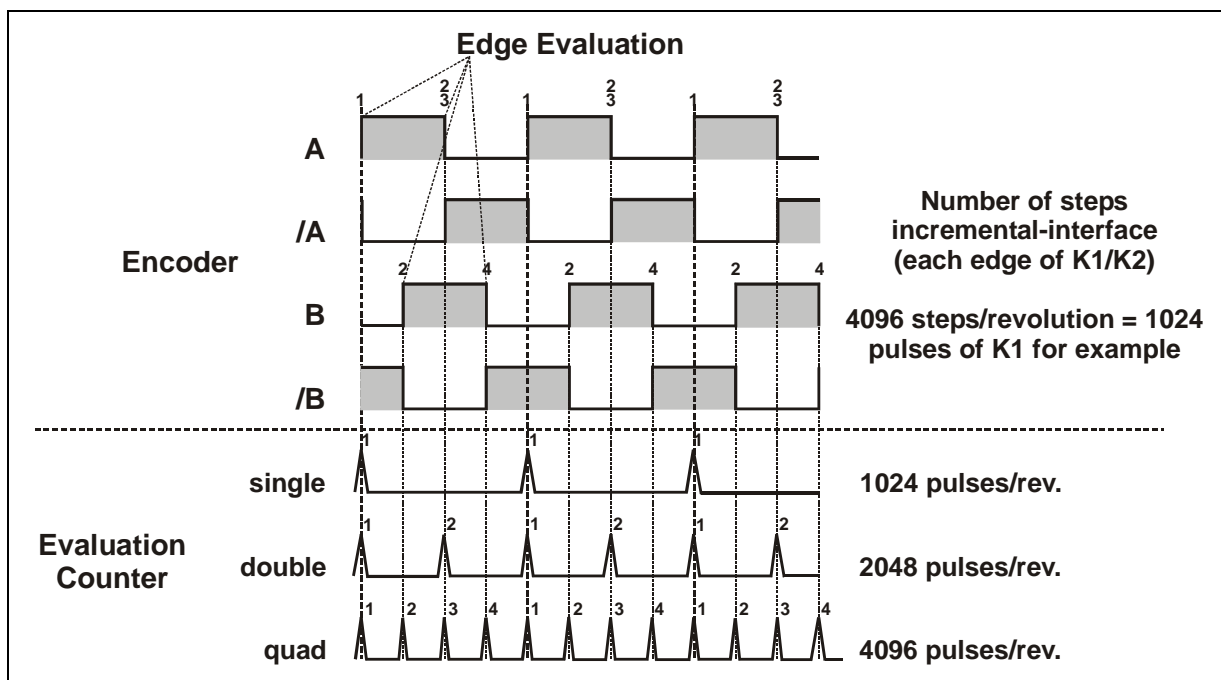
1.2 Definition of the number of steps of the incremental-interface and the pulse number resulting from it

Since the ISI-interface is an absolute-measurement-method, the output signal sequence (K1, K2) is not indicated in **pulses per revolution** as in the case of a pure incremental-encoder, but in **steps per revolution**. With the programming of the incremental-interface, therefore the **number of steps per revolution** is programmed!

That means:

The **programmed number of steps of the incremental-interface** corresponds to the **puls number after a quad-evaluation!**

Example:



Counter evaluations related to the above example

Single

Pulses/rev. = $0,25 \times \text{number of steps (incremental-interface)} = 0,25 \times 4096 = \underline{1024}$

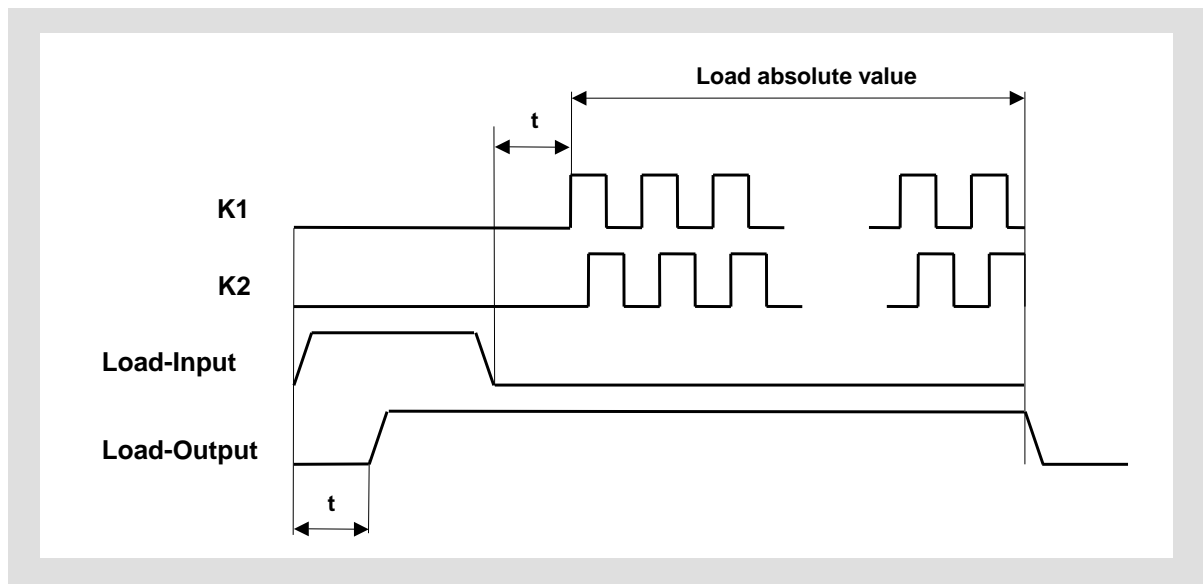
Double

Pulses/rev. = $0,5 \times \text{number of steps (incremental-interface)} = 0,5 \times 4096 = \underline{2048}$

Quad

The pulses per revolution corresponds to the number of steps (incremental-interface) of the encoder = 4096

1.3 Impulse diagram: loading process for falling loading edge



t = response time (programmable)